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Jennifer Volk-Fortier's practice focuses on the representation of high-technology clients in connection with patent prosecution in the electrical and mechanical fields. Her technical experience includes a wide range of electrical technologies including biomedical engineering, software development and robotics. She also has experience in post grant review proceedings before the United States Patent and Trademark Office (USPTO), including *inter partes review* (IPR) and covered business method (CBM) review proceedings.

While at George Washington School of Law, she served as an extern for the Honorable Judge Rader of the US Court of Appeals for the Federal Circuit. During her undergraduate studies at George Mason University, she was The Volgenau School of Information Technology and Engineering Undergraduate Student of the Year. She also co-authored several technical papers, which led to the development of a book chapter that was published in Intelligent Engineering Systems and Computational Cybernetics by Springer Publishing in early 2008.

Education

The George Washington University Law School JD, 2011

George Mason University BS, cum laude, Electrical Engineering, cum laude, 2007

Admissions & Credentials

District of Columbia

Virginia

Registered to practice before the United States Patent and Trademark Office (USPTO)